

Fig. 1 is a cross-sectional view of a semiconductor device. It shows a substrate (1) with a layer (2) and a patterned layer (3a, 3b, 3c) on top. A contact layer (4) is formed on the side of the patterned layer. Arrows indicate the direction of the layers.

Fig.1c

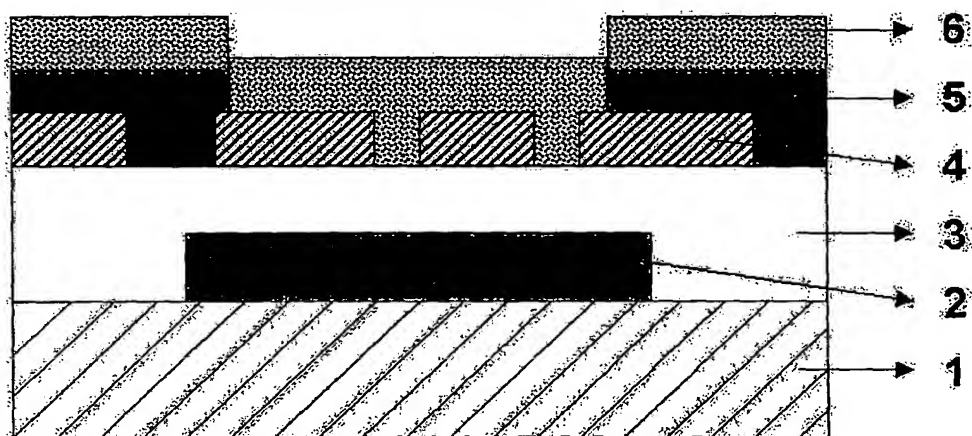


Fig.2

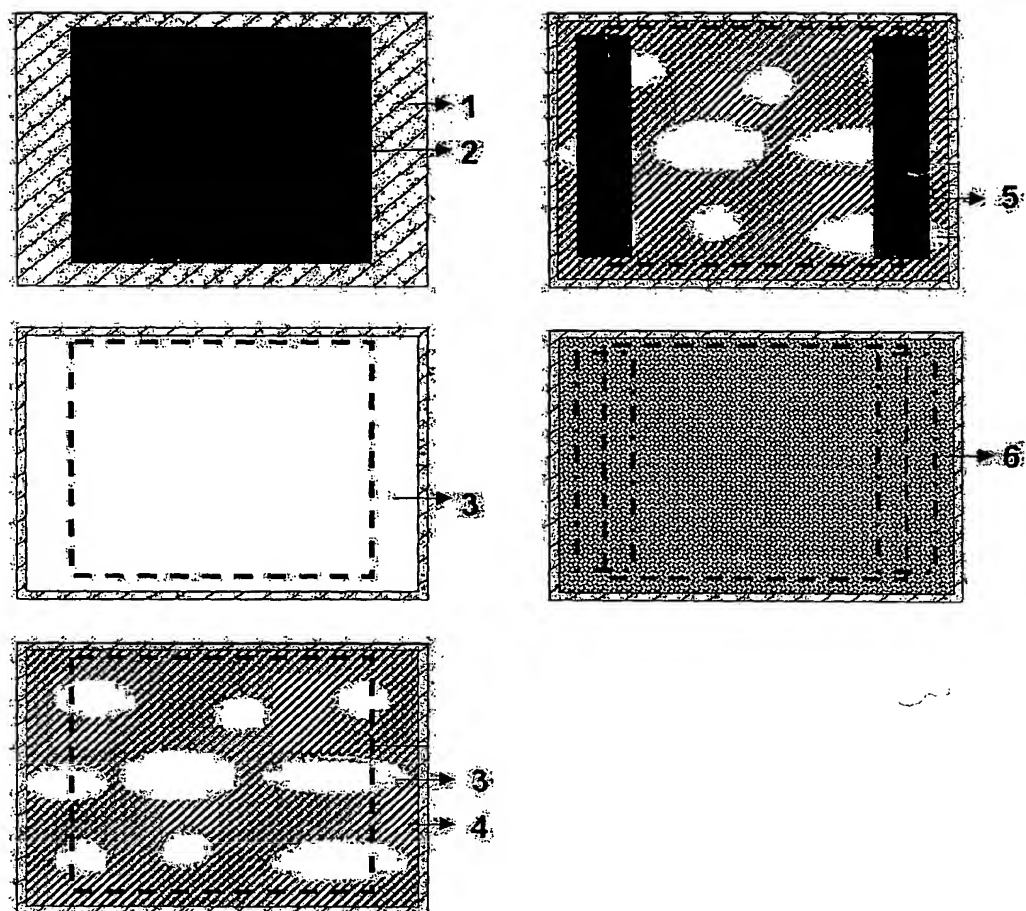


Fig.3

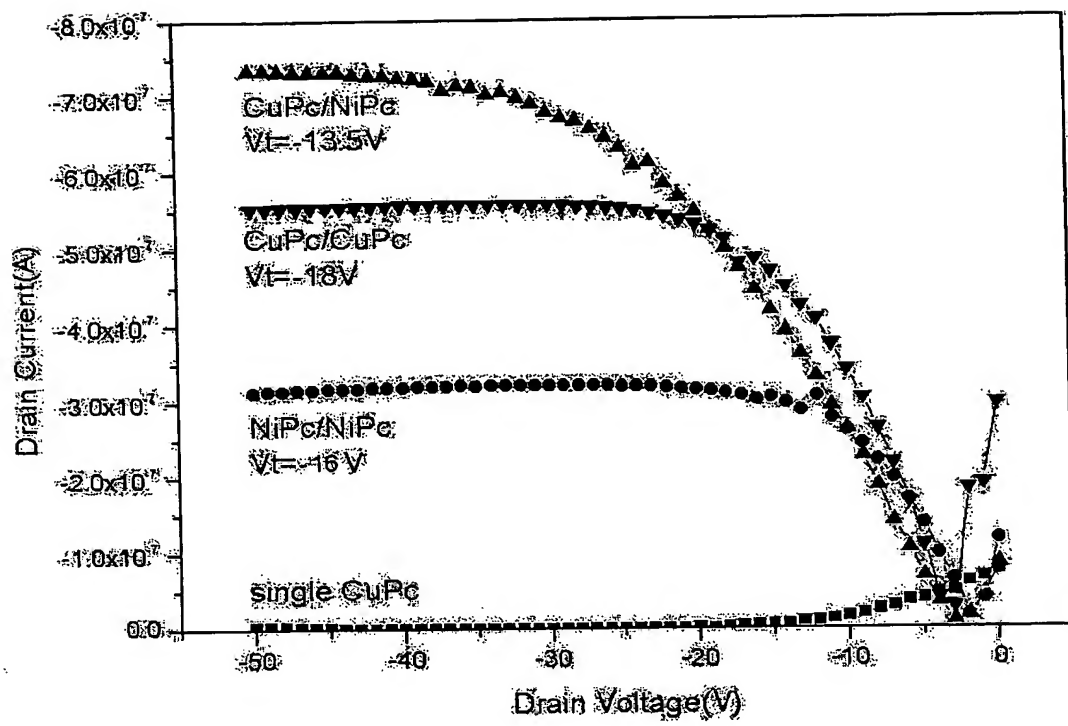


Fig.4

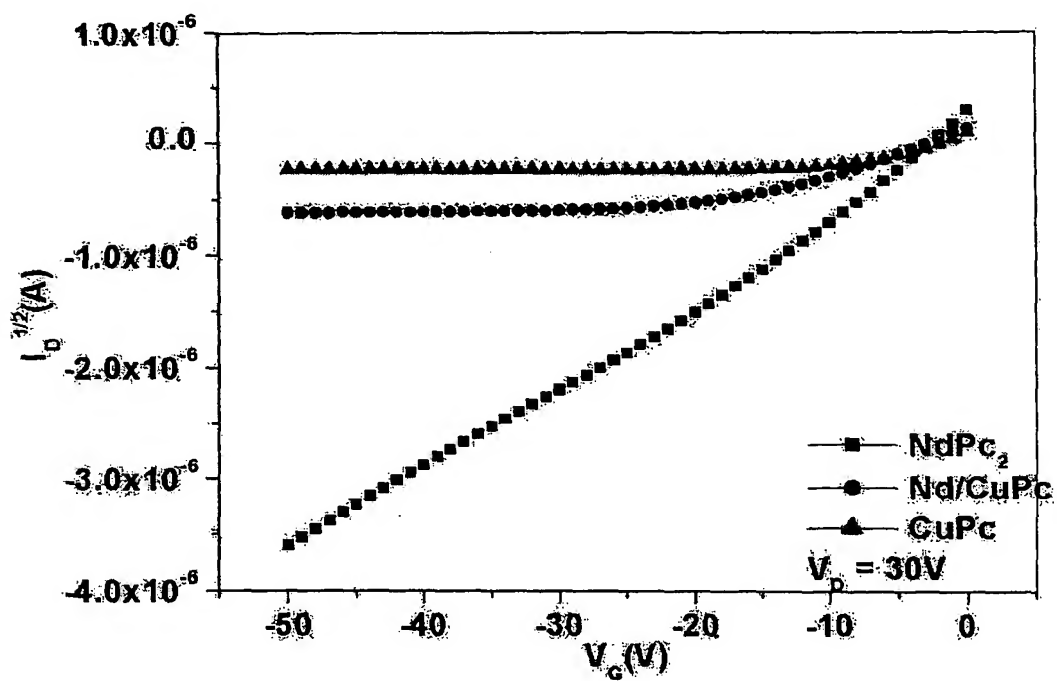


Fig.5a

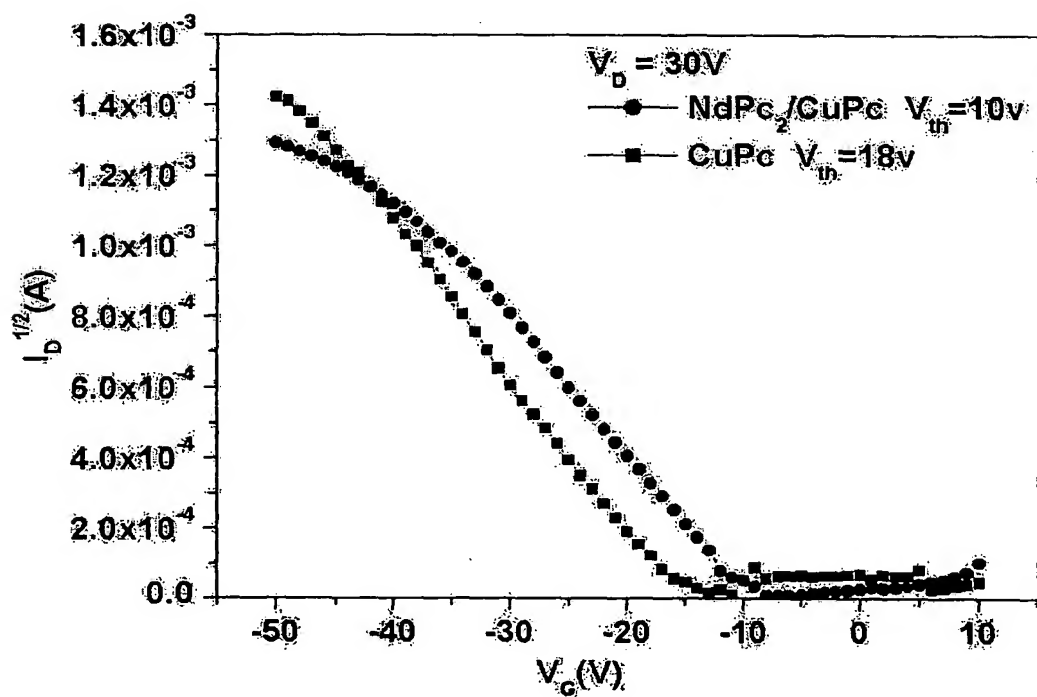


Fig.5b

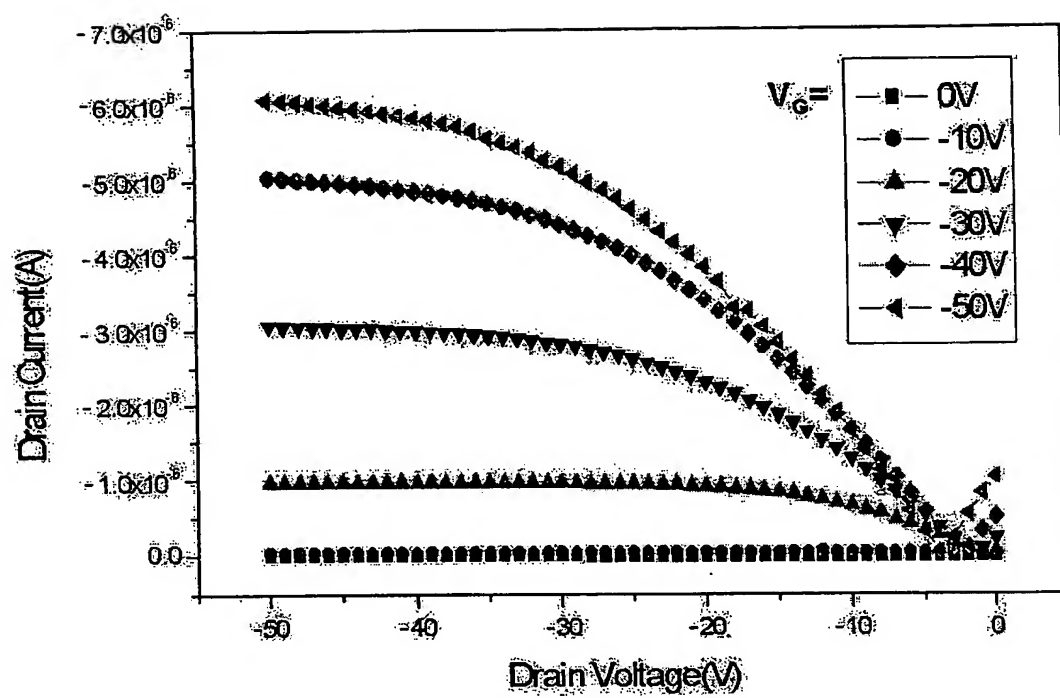


Fig.6